

## **Invertis University**

Bareilly-Lucknow National Highway, NH-24, Bareilly, Uttar Pradesh 243123

## **Course Outcomes (COs)**

| <b>Program Name:</b> | M. TECH (CSE)         | AY                    | 2022-23                  |
|----------------------|-----------------------|-----------------------|--------------------------|
| Course Name:         | Advanced Computer Arc | hitecture Class / Sem | 1 <sup>st</sup> Semester |
| Faculty Name:        | Ms. Yashi Bojpa       | al'                   |                          |
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## **Course Outcomes**

After completing this course, the student will be able to:

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| CO<br>Number | CO Statement   | Taxonomy   |
|--------------|--|------------|
| CO1          | Understand the fundamentals of parallel processing and distinguish between SIMD and MIMD models.                 | Understand |
| CO2          | Classify different parallel computing structures and identify their characteristics.                             | Analyze    |
| CO3          | Explore high-performance memory systems and their significance in computer architecture.                         | Analyze    |
| CO4          | Analyze the concept of pipelined computer systems and their role in enhancing processing efficiency.             | Analyze    |
| CO5          | Investigate processor architectures designed for parallel processing and their application in vector processing. | Evaluate   |

Taxonomy: Remember, Understand, Apply, Analyze, Evaluate, Create

Faculty Name and Signature

esculty of Engineering & Technology Invertis University

Bareilly-243123, UP

Registrar Invertis University