

Course Outcomes (COs)

Program Name:	B.Tech. (EE)	AY	2022-23
Course Name:	Digital Electronics	Class / Sem	3rd Semester
Faculty Name:	Ms. Sweta Agarwal		

Course Outcomes

After completing this course, the student will be able to:

CO Number	CO Statement	Taxonomy
CO1	Review the working of logic families and logic gates	Understand
CO2	Design and implement Combinational logic circuits.	Create
CO3	Design and implement Sequential logic circuits	Create
CO4	Explain the process of Analog to Digital conversion and Digital to Analog conversion	Understand
CO5	design and implement the given logical problem using PLDs	Create
CO6	Summarize Semiconductor memories and Programmable logic devices	Understand

Taxonomy: Remember, Understand, Apply, Analyse, Evaluate, Create



(Ms. Sweta Agarwal)

Faculty Name and Signature



Dean
Faculty of Engineering & Technology
Invertis University
Bareilly-243123, UP



Head
Department of ECE/EE
Invertis University
Bareilly-243123, UP



Registrar
Invertis University
Bareilly